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Address: Purba Medinipur ,West Bengal,India - 721657

Expertise

Telecommunications

MicroElectronics & VLSI Design

Work experience

1. Haldia Institute of Technology 2014 — Present

Assistant Professor
Purba Medinipur

Education

1. Ph.D - 2024

Mansarovar Global University,Bhopal

2. M.Tech - 2012

Haldia Institute of Technology

Membership In Professional Bodies

1. IEEE, 2024
Professional

Membership In Committees

Patent

1. The sentimental based disease estimation and health risk prediction system
Inventor(s): Rajrupa Metia, Santanu Maity, Razia Sultana, Amit Bhattacharya, Assignee: Haldia Institute of Technology
Published: 2022-07-08
2. LOW-POWER VLSI ARCHITECTURE FOR EDGE COMPUTING IN IOT ENVIRONMENTS
Inventor(s): Mr. Eswararao Boddepalli 2 . Ms. Rajrupa Metia 3 . Mr. Biswajit Das 4 . Mr. Santanu Maity 5 . Mrs. Razia Sultana, Assignee:
Published: 2024-02-02

Publication

1. Ventricular Dysfunction and Organ Transplant Rejection Determinant Biomarker Recognition in Serum via Charge Deduction Method
Amit Bhattacharyya, Proma Nag, Santanu Maity, Pinaki Satpathy, Papiya Debnath, Manash Chandra
IEEE, Volume , Year 2024, Pages
2. Design and Implementation of Low Power 6T SRAM using FINFET
Santanu Maity, Pinaki Satpathy, Mayank Mathur
Journal of Environmental Protection and Ecology, Volume 25, Year 2024, Pages 1664-1673
3. Enhancing Reliability in DMF Biochip Operations through Optimized Mixer Assignments and Mixing Modulation
Pinaki Satpathy, Santanu Maity, Mayank Mathur
Journal of Environmental Protection and Ecology, Volume 25, Year 2024, Pages 1649-1663

4. " A Nobel Approach of Designing A 4-bit Carry skip Adder using Reversible Logic

Pinaki Satpathy,Santanu Maity

Journal of VLSI Design Tools and Technology , Volume Voll.11, Issue 2, Year 2021, Pages

5. AF and DF Relay-Based Cooperative Spectrum Sensing Scheme with Dynamic Clustering

Aniket Kumar Sinha ., Amishu Kumar Barnwal ., Kuldeep Kumar ., Ankur Kumar ., Santanu Maity ., Chanchal Kr. De .,

Lecture Notes in Electrical Engineering, Volume 602, Year 2020, Pages 311-319

6. "Design and Implementation of 4 bit Vedic Multiplier

Santanu Maity

International Journal of Emerging in Science and Technology, Volume vol-03, Issue 05, , Year 2016, Pages pp-3865-3868

7. Design and Implementation of High-Speed, Low power Multipliers Using Reversible Logic

Santanu Maity

International Journal of Emerging in Science and Technology, Volume Vol-3, Issue-05,, Year 2016, Pages pp-3859-3864

8. 3. Comparative analysis and study on 4-bit RCA and CSA using CMOS logic

Santanu Maity

International Journal for Soft Computing and Engineering(IJSCE),, Volume Volume-4,Issue-6, Year 2015, Pages ISSN:2231-2307

9. High Performance Carry Skip Adder using Reversible Logic

International Journal of VLSI and Embedded Systems-IJVES , Year 2015

10. Design and Implementation of Low-power, High-performance Carry Skip Adder

Santanu Maity

International Journal of Engineering and Advanced Technology (IJEAT) , Volume Volume-1, Issue-4, , Year 2012, Pages ISSN:2249-8958

11. Design and Comparison of Multipliers using Different Logic Styles

Santanu Maity

International Journal of Soft Computing and Engineering (IJSCE) , Volume Volume-2, Issue-2, Year 2012, Pages ISSN:2231-2307

12. Analysis and Processing of Image Using Image Processing Technology

Santanu Maity

National Seminar in Communication and Signal Processing (NSCSP-12), , Volume , Year 2012, Pages

13. Image Coding and Compression Using Wavelet Technology

Santanu Maity

National Seminar in Communication and Signal Processing (NSCSP-12), , Volume , Year 2012, Pages

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